

### Abstract of the Disclosure

An LCD device includes a timing control circuit having a flicker reducing portion  
5 for detecting flickers in the entire one frame to be displayed in a liquid crystal panel and  
controlling a brightness of the backlight according to an amount of detected flickers. The  
timing control circuit can evidently reduce visually recognized flickers by dimming the  
brightness of the backlight when the detected flickers is more than given rate.